



501.32049RV1/329201392US4

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Yujiro KAJIHARA *et al.*
Serial No. : 09/987,978
Filed : 16 November 2001
For : LEAD FRAME SEMICONDUCTOR INTEGRATED
CIRCUIT DEVICE, USING THE SAME, AND METHOD
OF AND PROCESS FOR FABRICATING THE TWO
Art Unit : 2815
Examiner : J.B. Clark
Conf. No. : 7905

AMENDMENT

Mail Stop Reissue
Commissioner for Patents
POB 1450
Alexandria, Virginia 22313-1450

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20 January 2004

Sir:

In response to the *ex parte* Quayle Office Action having a USPTO mailing date of 18 November 2003, the following remarks are respectfully submitted in connection with the above-identified application.